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Bib Data Sheet

CONFIRMATION NO. 2338

SERIAL NUMBER 10/549,561	FILING OR 371(c) DATE 09/19/2005 RULE	CLASS 327	GROUP ART UNIT 2816	ATTORNEY DOCKET NO. DE 030089
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** CONTINUING DATA *****

This application is a 371 of PCT/IB04/00752 03/16/2004

*verified
RST*

** FOREIGN APPLICATIONS *****

EUROPEAN PATENT OFFICE (EPO) 03100720.6 03/20/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 05/11/2006

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY GERMANY	SHEETS DRAWING 2	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>[Signature]</i> Examiner's Signature	<i>[Signature]</i> Initials			

ADDRESS

24737

TITLE

Circuit arrangement and transistor control method

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